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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/677,343	10/03/2003	Tadashi Kosuga	Q77330	8770
23373	7590	02/24/2005	EXAMINER	
SUGHRUE MION, PLLC 2100 PENNSYLVANIA AVENUE, N.W. SUITE 800 WASHINGTON, DC 20037				HOLLINGTON, JERMELE M
ART UNIT		PAPER NUMBER		
		2829		

DATE MAILED: 02/24/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/677,343	KOSUGA, TADASHI	
	<b>Examiner</b>	<b>Art Unit</b>	
	Jermele M. Hollington	2829	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 03 October 2003.  
 2a) This action is FINAL.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-15 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-3, 6, 7, 10 and 12-15 is/are rejected.  
 7) Claim(s) 4-5, 8-9 and 11 is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1.) Certified copies of the priority documents have been received.  
 2.) Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3.) Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____  |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>11/04</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____                                    |

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-3, 6-7, 10 and 12-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Lloyd (GB 2251105A).

Regarding claim 1, Lloyd discloses [see Fig. 2] an electronic apparatus comprising: a specific circuit component (main body 11) having a circuit constant (bar code 14) included in a function circuit (printed circuit board 13) providing a specific circuit (11) function; and a measurement terminal (test pins 6 and electrical sensing circuit 7) for measuring the circuit constant (14); wherein the circuit constant (14) has a value in accordance with a specification.

Regarding claim 2, Lloyd discloses the circuit constant (14) of the specific circuit component (11) has a value suitable for identifying the specification and performing the specific circuit (11) function by the function circuit (13).

Regarding claim 3, Lloyd discloses an internal impedance measured via the measurement terminal (6 & 7) while the specific circuit component (11) is removed is higher than an impedance of the specific circuit component (11).

Regarding claim 6, Lloyd discloses the specific circuit component (11) is a resistor [see Figs. 1-2] having two terminals (wire mounting members 12), a capacitor having two terminals or a coil having two terminals.

Regarding claim 7, Lloyd discloses one terminal (12) of the specific circuit component (11) is connected to an external terminal (test pin 6).

Regarding claim 10, Lloyd discloses the specification is identification information of a destination of the electronic apparatus [see page 2, line 15- page 3, line 19].

Regarding claim 12, Lloyd discloses a specification identification method for an electronic apparatus including a specific circuit component (main body 11) having a circuit constant (bar code 14) included in a function circuit (printed circuit board 13) having a specific circuit function and a measurement terminal (test pins 6) for measuring the circuit constant (14), the method comprising: measuring the circuit constant (14) by connecting the measurement terminal (6) with a measuring apparatus (electrical sensing circuit 7); and identifying a specification in accordance with a value of the measured circuit constant (14).

Regarding claim 13, Lloyd discloses the identification step includes comparing the circuit constant (11) with a constant that is predetermined based on the individual specification and determining the specification by using the comparison result.

Regarding claim 14, Lloyd discloses a manufacturing method for an electronic apparatus including a specific circuit component (main body 11) having a circuit constant (bar code 14) included in a function circuit (printed circuit board 13) having a specific circuit function and a measurement terminal (test pin 6) for measuring the circuit constant (14) the method comprising: providing the specific circuit component (11) and the measurement terminal (6); measuring the circuit constant (11) by connecting the measurement terminal (6) with a measuring apparatus (electrical sensing circuit 7); and identifying a specification in accordance with a value of the measured circuit constant (14).

Regarding claim 15, Lloyd discloses adjusting and/or inspecting the electronic apparatus in accordance with the identified specification [see page 2, line 15- page 3, line 19].

*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jermele M. Hollington whose telephone number is (571) 272-1960. The examiner can normally be reached on M-F (9:00-4:30 EST) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nestor Ramirez can be reached on (517) 272-2034. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

*Jermele M. Hollington*  
Jermele M. Hollington  
Patent Examiner  
Art Unit 2829

JMH  
February 18, 2005